

IRIS: A Portable Runtime System Exploiting Multiple Heterogeneous Programming Systems

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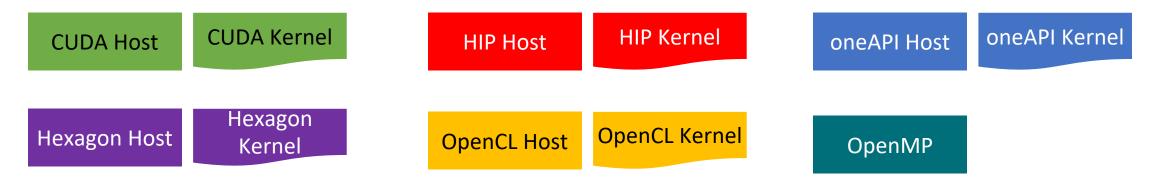
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No De Facto Standard for Heterogeneous Programming

• ORNL Experimental Computing Laboratory (ExCL) systems*

Systems	Snapdragon	Jetson	Zynq		DGX		0	swa	ld	Summit	Fror	tier
CPU	ARM	ARM	ARM	I	I	Т	I	I	I.	IBM	A٨	1D
GPU	Qualcomm	NVIDIA		N	IVIDI	A	NV		NV	NVIDIA	AMD	AMD
FPGA			Xilinx				Inte		ntel			
DSP	Qualcomm											





* ORNL ExCL: https://excl.ornl.gov/

We Need Portability in Heterogeneous Programming

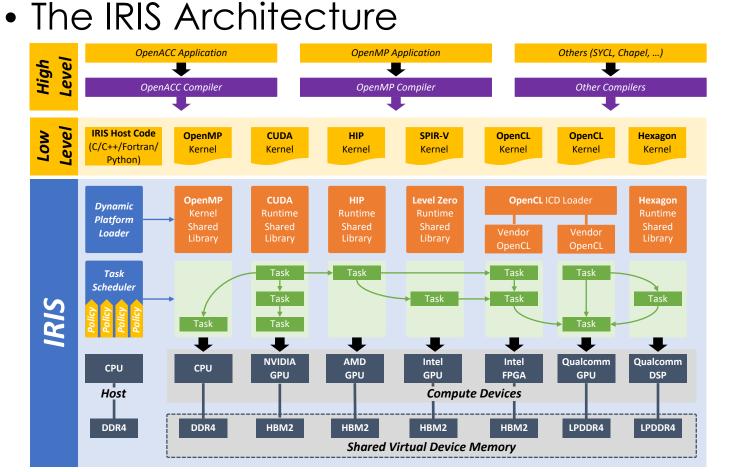
• Not portable program across different HW configurations

Systems	Snapdragon	Jetson	Zynq		DGX		0	swal	d	Summit	t	Fror	ntier
CPU	ARM	ARM	ARM	- I	I	- I	- I	I	- I	IBM		AN	/ID
GPU	Qualcomm	NVIDIA		N	IVIDI	A	NV	,	NV	NVIDIA		AMD	AMD
FPGA			Xilinx				Inte	el li	ntel				
DSP	Qualcomm												
	Snapdragor		IP Kernel						Fron Ho		Op	oenMP	Kernel

	Snapdragon Host	Openivip Kerner	Host	Оренийр кетпег
	OpenMP + OpenCL +	OpenCL Kernel	OpenMP + HIP	HIP Kernel
K RIDGE	Hexagon	Hexagon Kernel		
N INIDGE				

National Laboratory

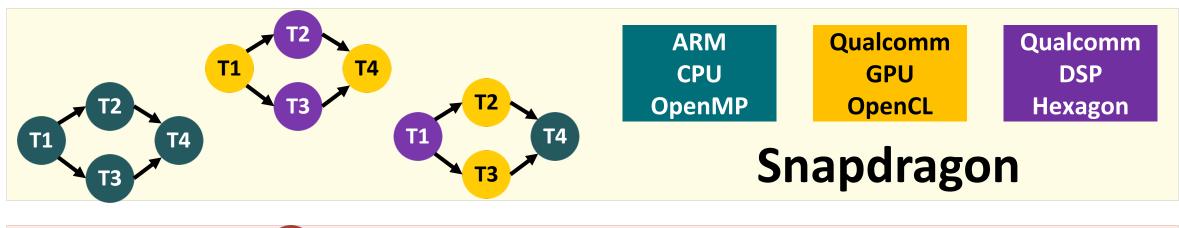
Orchestrating Multiple Programming Systems

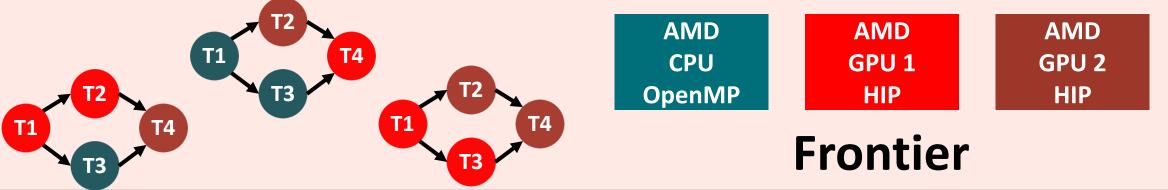


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- Compilers
 - High level application →
 IRIS unified host code + native kernels
- Dynamic Platform Loader
 - Automatically discover all available accelerators and their programming systems
- Task Scheduler
 - Task: memory copy + kernel launch
 - DAG-style tasks graph across multiple devices
 - Device selection policies
- Shared Virtual Device Memory (SVDM)
 - An Illusion of single logical device memory across all physical device memories
 - Multiple local copies on multiple device memories (relaxed consistency model)

Unified Host + Multiple Native Kernels + Shared VDM → Flexible Task Scheduling & Portable Application



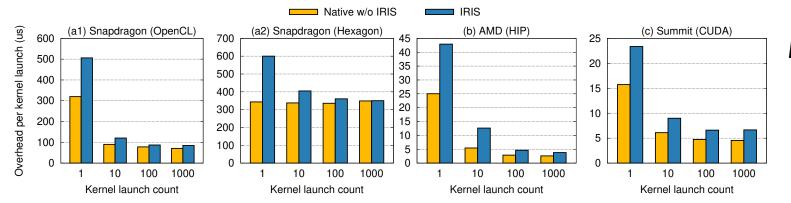


- A task can be freely scheduled and run on any device.
- An IRIS application is portable across different heterogeneous systems.

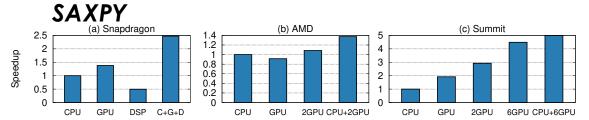


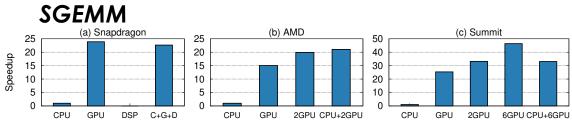
Evaluation

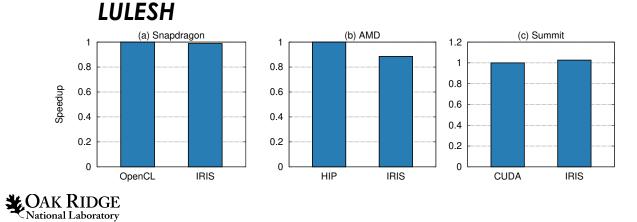
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Kernel Launch Overhead







Systems	Snapdragon	AMD	Summit		
CPU	ARM	AMD	IBM		
	OpenMP	OpenMP	OpenMP		
GPU	Qualcomm	AMD	NVIDIA		
	OpenCL	HIP	CUDA		
DSP	Qualcomm Hexagon				

Recap

Situation No de facto standard for heterogeneous programming

- TaskAchieving portability in heterogeneous programming
- A new portable runtime system, IRIS
 - Orchestrating multiple programming systems (CUDA, Hexagon, HIP, Level Zero, OpenCL, OpenMP)
 - Unified Host + Multiple Native Kernels + Shared Virtual Device Memory → Flexible Task Scheduling & Portable Application
- **Result** IRIS achieves portability, programmability, and performance

IRIS is an open source software

https://github.com/ORNL/iris



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